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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/989,761	11/19/2001	Kenneth Y. Ogami	CYPR-CD01179M	2006

7590 08/26/2004  
WAGNER, MURABITO & HAO LLP  
Third Floor  
Two North Market Street  
San Jose, CA 95113

EXAMINER


DO, THUAN V

ART UNIT PAPER NUMBER

2825

DATE MAILED: 08/26/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 09/989,761	<b>Applicant(s)</b> OGAMI ET AL.	
	<b>Examiner</b> Thuan Do	<b>Art Unit</b> 2825	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 17 June 2004.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-28 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

### DETAILED ACTION

1. This final office action is responsive to amendment entered on 06/17/2004.  
Claims 1-28 are pending in this office action.

#### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

- (e) the invention was described in-
- (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or
  - (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).

2. Claims 1-28 are rejected under 35 U.S.C. 102(e) as being unpatentable over Heile et al. Pat. No. 6321369.

**Regarding claim 1:** Heile teaches a method comprising:

selecting the global electronic resource via an input device wherein the global electronic resource is associated with a first electronic design project (col. 5, lines 30-34) ;

displaying a plurality of parameter values which can be chosen for the global electronic resource in response to said selecting (col. 5, lines ) ;

choosing one of the plurality of parameter values as a chosen parameter value for the global electronic resource via the input device (col. 14, lines 23-42); and

storing the chosen parameter value as a default global setting for use by second electronic design project (col. 10, lines 65-67).

**Regarding claim 2:** Heile teaches a method with second electronic design project (Figure 1, at least box 20).

**Regarding claim 3:** Heile teaches a method with displaying pop-up list (col. 7, lines 27-40).

**Regarding claim 4:** Heile teaches a method with parameter values (col. 14, lines 23-42).

**Regarding claim 5:** Heile teaches a method with input device (col. 14, lines 23-42).

**Regarding claims 6:** Heile teaches a method with chosen parameter value (col. 14, lines 23-42).

**Regarding claims 7,17,23:** These claims teach the method, program or system similar to the method of claim 1 and rejected in the similar manner.

**Regarding claims 8,9,10:** Heile teaches a method with input device (col. 5, lines 30-34).

**Regarding claim 11:** Heile teaches a method with pop-up list (col. 7, lines 27-40).

**Regarding claim 12:** Heile teaches a system for the similar method of claim 1 and rejected in the same rationale except tracking a location function is taught in the column 20, lines 1-5 using a trackball.

**Regarding claim 13:** Heile teaches a method with global parameters (col. 9, lines 15-22).

**Regarding claims 14,15,16:** Heile teaches a method with input device (col. 5, lines 30-34).

**Regarding claims 18,19:** Heile teaches a method with selecting (col. 5, lines 30-34).

**Regarding claim 20:** Heile teaches a method with updating a memory and propagating global electronic design (Figure 1 at least box 20).

**Regarding claim 21:** Heile teaches a method with microcontroller circuit (Figure 5A).

**Regarding claim 22:** Heile teaches a method with pop-up list (col. 7, lines 27-40).

**Regarding claim 24:** Heile teaches a system with selecting (col. 5, lines 30-34).

**Regarding claim 25:** Heile teaches a system with cursor device (col. 20, lines 1-5).

**Regarding claim 26:** Heile teaches a system with updating a memory and propagating global electronic design (Figure 1 at least box 20).

**Regarding claim 27:** Heile teaches a system with microcontroller circuit (Figure 5A).

**Regarding claim 28:** Heile teaches a system with pop-up list (col. 7, lines 27-40).

### ***Response to Arguments***

Applicant's arguments have been considered but not persuasive for the following reasons:

Applicant said that Heile does not teach selecting the global electronic resource via an input device wherein the global electronic resource is associated with a first programmable microcontroller circuit.

Heile teaches that the user can select a particular design by using user interface in co. 5, lines 30-33 where the particular design including global electronic resource such as global parameters in col. 10, lines 64-67 and the user or designer can select particular input devices as an assignment for user interface in col. 18, lines 9-22 and within the programmable circuit devices in col. 5, lines 1-10. Those areas meet the limitation of claimed invention of selecting the global electronic resource via an input device wherein the global electronic resource is associated with a first programmable microcontroller circuit.

Applicant said that Heile does not teach storing the selected parameter value as a default global electronic setting for use by a second programmable microcontroller circuit.

Heile teaches the parameter-based project variations suitable for use with this invention will be stored in a global project default file for specific design in col. 10, lines 64-67 that limitation meets the claimed invention.

Applicant said that Heile does not teach tracking a location within the associated electronic hardware resource.

Heile teaches selecting resource database of proper components and their locations for meeting requirements in col. 10, lines 64-67; col. 18, lines 9-22 and col. 3, lines 23-35 that meets the limitation of claimed invention of tracking (selecting and using location) a location within the associated electronic hardware resource.

Applicant said that Heile does not teach displaying in tabular form a list of global electronic design resources and respective global design parameter values associated therewith for use in a first programmable microcontroller circuit.

Heile teaches display in figure 4D, a tabular form in col. 4, lines 65-67, an electronic design resources and respective global design parameter values in col. 10, lines 64-67 and col. 18, lines 9-22 and associated therewith for use in a first programmable microcontroller circuit in col. 5, lines 1-10.

**THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

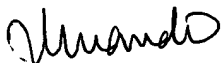
A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

### CONTACT INFORMATION

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan Do whose telephone number is 571-272-1891. The examiner can normally be reached on Monday-Friday 8:30-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 571-272-1907. The fax phone numbers for the organization where this application or proceeding is assigned are 703 305-3431 for regular communications and 703-305-3431 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0596.



Thuan Do  
Patent examiner  
8/24/04